



## Product Change Notification / SYST-22BSZA853

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### Date:

24-Mar-2021

### Product Category:

32-bit Microcontrollers, USB Power Delivery

### PCN Type:

Document Change

### Notification Subject:

Data Sheet - SAM D20 Datasheet

### Affected CPNs:

[SYST-22BSZA853\\_Affected\\_CPN\\_03242021.pdf](#)

[SYST-22BSZA853\\_Affected\\_CPN\\_03242021.csv](#)

### Notification Text:

SYST-22BSZA853

Microchip has released a new Product Documents for the SAM D20 Datasheet of devices. If you are using one of these devices please read the document located at [SAM D20 Datasheet](#).

**Notification Status:** Final

#### Description of Change:

- Pinout - Updated the pinout for UFPGA64 to show the GND pins as black in color.
- GCLK - Updated the GENDIV register with a Write-Synchronized property and added a new column to the table under the DIV bit field
- SYSCTRL - Updated the AMPGC and GAIN bits of the XOSC register with a new text
- EVSYS - Updated the Features with a new text for the last line item
- NVMCTRL - Updated the LOCK register with a new reset value
- ADC - Updated Power Management and Sleep Mode Operation. ADC OVERRUN interrupt cannot wake up the device from Sleep mode.
- DAC
  - Updated the Block Diagram to represent ADC input
  - Updated the CTRLB Register with a new table value for the EOEN bit

#### Electrical Specifications at 85

The following updates were performed:

- Under Absolute Maximum Ratings, the GPIO Clusters table was updated with revised information for new packages
  - Added Note 1 to Current Consumption - Device Variant A
  - Added Note 2 to Current Consumption - Device Variant B
  - Updated the Maximum Peripheral Clock Frequencies table in Maximum Clock Frequencies
  - Updated the POR Operating Principle Diagram in Power-On Reset (POR) Characteristics
  - Updated the Operating Conditions table in Digital-to-Analog Converter (DAC) Characteristics with a new note
  - Updated the equation in XOSC Crystal Oscillator Characteristics
  - Updated XOSC32K Digital Clock Characteristics with a new note
  - Updated XOSC32K Crystal Oscillator Characteristics with a new text
- Electrical Specifications at 105

The following updates were performed:

- Under Absolute Maximum Ratings, a new information was added
- Updated the Maximum Peripheral Clock Frequencies table in Maximum Clock Frequencies
- Updated the POR Operating Principle Diagram in Power-On Reset (POR) Characteristics
- Updated the Operating Conditions table in Digital-to-Analog Converter (DAC) Characteristics with a new note
- Updated the equation in XOSC Crystal Oscillator Characteristics
- Updated XOSC32K Digital Clock Characteristics with a new note
- Updated XOSC32K Crystal Oscillator Characteristics with a new text

AEC-Q100 Electrical Specifications • Added Electrical Specifications chapter for AEC-Q100

**Impacts to Data Sheet:** None

**Reason for Change:** To Improve Manufacturability

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 24 Mar 2021

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

## Attachments:

[SAM D20 Datasheet](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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Affected Catalog Part Numbers (CPN)

ATSAMD20E14A-AN  
ATSAMD20E14A-ANT  
ATSAMD20E14A-AU  
ATSAMD20E14A-AUA1  
ATSAMD20E14A-AUA2  
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